

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	0	"clock feedback path" near2 "allocation circuit"	JPO	OR	OFF	2004/01/21 16:26
S2	1	"clock feedback path"	JPO	OR	OFF	2004/01/21 16:28
S3	1	"10301663"	JPO	OR	OFF	2004/01/21 16:28
S4	39	clock adj buffer adj delay\$1	USPAT	OR	OFF	2004/01/17 15:14
S5	333256	(rc resistive-capacitive) delay\$1	USPAT	OR	OFF	2004/01/17 15:18
S6	39	(clock adj buffer adj delay\$1) and ((rc resistive-capacitive) delay\$1)	USPAT	OR	OFF	2004/01/17 15:15
S7	39	(clock adj buffer adj delay\$1) same ((rc resistive-capacitive) delay\$1)	USPAT	OR	OFF	2004/01/17 15:15
S8	0	(clock adj buffer adj delay\$1) same ((rc resistive-capacitive) delay\$1) same (synchron\$6 (align\$3 near phase))	USPAT	OR	OFF	2004/01/17 15:16
S9	10	(clock adj buffer adj delay\$1) same ((rc resistive-capacitive) delay\$1) same synchron\$6	USPAT	OR	OFF	2004/01/17 15:17
S10	2225	(rc resistive-capacitive) adj delay\$1	USPAT	OR	OFF	2004/01/17 15:18
S11	0	(clock adj buffer adj delay\$1) same ((rc resistive-capacitive) adj delay\$1)	USPAT	OR	OFF	2004/01/17 15:18
S12	3	(clock adj buffer adj delay\$1) and ((rc resistive-capacitive) adj delay\$1)	USPAT	OR	OFF	2004/01/17 15:20
S13	1	"5268656".pn.	USPAT	OR	OFF	2004/01/17 15:21
S14	165	(return\$3 feedback) near3 clock near3 path	USPAT	OR	OFF	2004/01/20 13:16
S15	0	((return\$3 feedback) near3 clock near3 path) same (travel adj time)	USPAT	OR	OFF	2004/01/19 15:07
S16	1	((return\$3 feedback) near3 clock near3 path) same (round adj trip)	USPAT	OR	OFF	2004/01/19 15:09
S17	10	((round adj trip) near2 (travel adj time)) with clock	USPAT	OR	OFF	2004/01/20 11:03
S18	4494	(return\$3 feedback) near3 clock	USPAT	OR	OFF	2004/01/19 15:06
S19	4	((return\$3 feedback) near3 clock) same (travel adj time)	USPAT	OR	OFF	2004/01/19 15:07
S20	14	((return\$3 feedback) near3 clock) same (round adj trip)	USPAT	OR	OFF	2004/01/19 15:09
S21	15	(round adj trip) same (clock near3 travel\$4)	USPAT	OR	OFF	2004/01/20 13:11
S22	5	(round adj trip) same (clock near3 travel\$4)	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/20 11:05

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S23	42	(return\$3 feedback) near3 clock near3 path	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/20 11:16
S24	1	"6014048".pn.	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/20 11:16
S25	1	"6014048".pn.	USPAT	OR	OFF	2004/01/20 13:08
S26	1	"5298866".pn.	USPAT	OR	OFF	2004/01/20 13:10
S27	2725	713/400-601.ccls.	USPAT	OR	OFF	2004/01/20 13:10
S28	335	713/400-601.ccls. and (clock near3 distribut\$4)	USPAT	OR	OFF	2004/01/20 13:10
S29	0	(713/400-601.ccls. and (clock near3 distribut\$4)) and ((round adj trip) same (clock near3 travel\$4))	USPAT	OR	OFF	2004/01/20 13:11
S30	7	(713/400-601.ccls. and (clock near3 distribut\$4)) and (round adj trip)	USPAT	OR	OFF	2004/01/20 13:12
S31	7	(713/400-601.ccls. and (clock near3 distribut\$4)) and ((return\$3 feedback) near3 clock near3 path)	USPAT	OR	OFF	2004/01/21 14:45
S32	4	phase\$1 near2 equal near3 align\$2	USPAT	OR	OFF	2004/01/21 14:45
S33	102	(clock near3 delay) with estimat\$3	USPAT	OR	OFF	2004/12/03 16:00
S34	2485	(rc resistive-capacitive) adj delay\$1	USPAT	OR	OFF	2004/12/03 11:33
S35	0	S34 same S33	USPAT	OR	OFF	2004/12/03 11:33
S36	4	S34 and S33	USPAT	OR	OFF	2004/12/03 11:35
S37	184	(return\$3 feedback) near3 clock near3 path	USPAT	OR	OFF	2004/12/03 13:16
S38	0	S33 same S37	USPAT	OR	OFF	2004/12/03 11:35
S39	4	S33 and S37	USPAT	OR	OFF	2004/12/03 11:35
S40	37	("5298866").URPN.	USPAT	OR	OFF	2004/12/03 11:47
S41	3	S40 and estimat\$3	USPAT	OR	OFF	2004/12/03 11:49
S42	8	("4755704" "4868522" "4899071" "5103192" "5118975" "5124569" "5164677" "5221863").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2004/12/03 11:49
S43	0	S42 and estimat\$3	USPAT	OR	OFF	2004/12/03 11:49
S44	1	(return\$3 feedback) near3 clock near3 path and 716/6.ccls.	USPAT	OR	OFF	2004/12/03 12:29
S45	766	716/6.ccls.	USPAT	OR	OFF	2004/12/03 12:29
S46	10	716/6.ccls. and S33	USPAT	OR	OFF	2004/12/03 16:00
S47	41	(clock near3 delay) with estimat\$3	USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/03 13:16

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S48	59	(return\$3 feedback) near3 clock near3 path	USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/03 13:17
S49	0	S47 and S48	USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/03 13:17
S50	1779	(return\$3 feedback) near3 clock	USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/03 13:17
S51	0	S47 and S49	USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/03 13:17
S52	1	S47 and S50	USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/03 13:17
S53	0	((time near2 (consumption consum\$4)) faster ??expensive) same S33	USPAT	OR	OFF	2004/12/03 15:02
S54	51	((time near2 (consumption consum\$4)) faster ??expensive) and S33	USPAT	OR	OFF	2004/12/03 15:03
S55	0	716/6.ccls. and (S33 with round)	USPAT	OR	OFF	2004/12/03 15:59
S56	64	(clock near3 delay) near3 estimat\$3	USPAT	OR	OFF	2006/09/29 18:09
S57	9	716/6.ccls. and S56	USPAT	OR	OFF	2004/12/03 16:00
S58	113	(clock near3 delay) near3 estimat\$3	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/29 18:09
S59	39	(clock adj2 delay) near3 estimat\$3	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/29 18:10
S60	0	(clock adj2 delay) near3 estimat\$3 same resistive	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/29 18:09